



	Subclass	ISSUE CLASSIFICATION
	Class	

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PATENT NUMBER

U.S. UTILITY Patent Application

O.I.P.E.	PATENT DATE
SCANNED _____ Q.A. _____	

APPLICATION NO. 09/603147	CONT/PRIOR	CLASS 438	SUBCLASS 396	ART UNIT 2822 2842	EXAMINER KELIN
APPLICANT'S John Moore Guy Blalock Scott DeBoer					
TITLE Capacitor constructions, DRAM constructions, and semiconductive material assemblies					

PTO-2040
12/89

ISSUING CLASSIFICATION

Continued on Issue Slip Inside File Jacket

TERMINAL DISCLAIMER	DRAWINGS			CLAIMS ALLOWED	
	Sheets Drwg.	Figs. Drwg.	Print Fig.	Total Claims	Print Claim for O.G.
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	(Assistant Examiner)	(Date)		Amount Due	Date Paid
<input type="checkbox"/> The terminal _____ months of this patent have been disclaimed.				ISSUE BATCH NUMBER	
	(Primary Examiner)	(Date)			
	(Legal Instruments Examiner)	(Date)			

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